

ELE 373: Advanced Digital Architecture and Design

April 7, 2009

Due date: April 23, 2009

Project 3.

Design, test (using Abel test vectors and in-circuit) a 4-bit accumulator-adder. Your circuit is supposed permit the accumulation of a sum using complement arithmetic from a 4-bit switch serving as the input. Include control inputs that would allow a user to reset your circuit and to initiate a single addition. Include overflow detection (optional).