CMOS Small Signal Inverting Amplifier Analysis & Design:

1. Allen&Holberg P5.1-7
2. Allen&Holberg P5.1-9
3. Allen&Holberg P5.1-10
4. Design a Current-Source Load Amplifier like in Figure5.1-10 for a small signal gain of 100 and 3-dB cut-off frequency of 10 KHz if the load capacitance is 10 pF. Use 2um N-Well CMOS technology files supplied as “scna20” for calculations. Verify that it works with PSpice.
5. Design the layout of your amplifier in Problem 4 using L-Edit.

Submit your files (PSpice/Word/Mathematica and LEDIT) electronically through eelinux "web-submit" process.